

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING SENT VIA Facsimile Transmission to: COMMISSIONER OF  
PATENTS AND TRADEMARKS, Alexandria VA. 22313, on July 26, 2005 by: Teri McDonald

 7/26/05  
Signature & Date

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: John E. Barth et al.	Date: July 26, 2005
Serial Number: 10/757,846	Examiner: Son Luu Mai
Filed: 1/15/2004	Group Art Unit: 2827
Title: Concurrent Refresh Mode with Distributed Row Address Counters in an Embedded DRAM	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION**

Commissioner of Patents and Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated July 13, 2005, the following amendment is being submitted to place the above identified application in prima facie condition for allowance.

Please amend the above identified application as follows:

10/757,846

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FIS920030409US1

Entry is approved.  
JL